



KFLR.0012P

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jorge Alberto Grilo ) Group Art Unit: Unknown  
App. No. : 10/688,005 )  
Filed : October 16, 2003 )  
For : METHOD AND APPARATUS FOR )  
INCREASING THE LINEARITY )  
AND BANDWIDTH OF AN )  
AMPLIFIER )  
Examiner : Unknown )

I hereby certify that this correspondence and all marked  
attachments are being deposited with the United States Postal  
Service as first-class mail in an envelope addressed to:  
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-  
1450 on

January 14, 2004

*C. Miller*  
Chad W. Miller, Reg. No. 44,943

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. This Information Disclosure Statement is being filed before the mailing of the first Office Action on the merits, and as such, no fee is required in accordance with 37 C.F.R. § 1.97(b)(3).

Respectfully submitted,

Dated: 1/14/04By: *C. Miller*

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| FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE<br>PATENT AND TRADEMARK OFFICE<br>JAN 20 2004<br>INFORMATION DISCLOSURE STATEMENT<br>BY APPLICANT<br>(USE SEVERAL SHEETS IF NECESSARY) | ATTY. DOCKET NO.<br>SLRFLR.0012P | APPLICATION NO.<br>10/688,005 |
|  | APPLICANT<br>Jorge Alberto Grilo |                               |
|  | FILING DATE<br>October 16, 2003  | GROUP<br>Unknown              |

| U.S. PATENT DOCUMENTS |  |                 |      |      |       |          |                                 |
|-----------------------|--|-----------------|------|------|-------|----------|---------------------------------|
| EXAMINER<br>INITIAL   |  | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE<br>(IF APPROPRIATE) |
|                       |  |                 |      |      |       |          |                                 |
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| FOREIGN PATENT DOCUMENTS |  |                 |      |         |       |          |             |    |
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| EXAMINER<br>INITIAL      |  | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION |    |
|                          |  |                 |      |         |       |          | YES         | NO |
|                          |  |                 |      |         |       |          |             |    |
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| EXAMINER<br>INITIAL | OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) |  |
|---------------------|--|--|
|                     | /  | Huiting Chen, et al., "Current Mirror Circuit with Accurate Mirror Gain for Low $\beta$ Transistors", 4 pages (Unknown Date)   |
|                     | /  | G. Palmisano, et al., "Harmonic Distortion on Class AB CMOS Current Output Stages", <i>IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing</i> , Vol. 45, No. 2, February 1998, pp. 243-250 |
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| EXAMINER | DATE CONSIDERED |
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\*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.